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Mark Becker	(Depositor's name)
<i>Mark Becker</i>	(Signature)
8/30/04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,042	06/20/2003	Craig Bingert	AG061C1	6389

TITLE OF INVENTION: INTEGRATED CIRCUIT AND ASSOCIATED DESIGN METHOD USING SPARE GATE ISLANDS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	11/26/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
MAI, LAM T	2819	326-041000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  
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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Lattice Semiconductor Corp.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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- Publication Fee (No small entity discount permitted)
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- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date *8/30/04*

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